# N-Channel 650-V (D-S) MOSFET

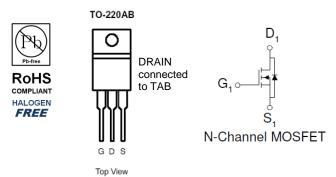
### **Key Features:**

- Low r<sub>DS(on)</sub> trench technology
- · Low thermal impedance
- · Fast switching speed

### **Typical Applications:**

- Power Supplies
- Motor Drives
- Consumer Electronics

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	$r_{DS}(V)$ $r_{DS(on)}(m\Omega)$		
650	650 @ V <sub>GS</sub> = 10V	14 <sup>a</sup>	



ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^{\circ}$ C UNLESS OTHERWISE NOTED)						
Parameter		Symbol	Limit	Units		
Drain-Source Voltage		$V_{DS}$	650	V		
Gate-Source Voltage		$V_{GS}$	±30	V		
Continuous Drain Current a	T <sub>C</sub> =25°C	I <sub>D</sub>	14	Α		
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	60	^		
Continuous Source Current (Diode Conduction) <sup>a</sup> T <sub>C</sub> =25°C		I <sub>S</sub>	14	Α		
Power Dissipation <sup>a</sup>	T <sub>C</sub> =25°C	$P_{D}$	300	W		
Operating Junction and Storage Temperature Range			-55 to 175	°C		

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Maximum	Units
Maximum Junction-to-Ambient °	$R_{\theta JA}$	62.5	°C/W
Maximum Junction-to-Case	$R_{\theta JC}$	0.5	C/VV

#### Notes

- a. Package Limited
- b. Pulse width limited by maximum junction temperature
- c. Surface Mounted on 1" x 1" FR4 Board.

### **Electrical Characteristics**

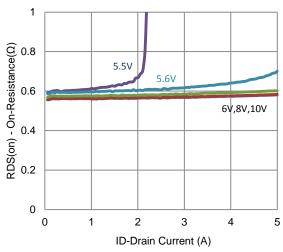
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 250 \text{ uA}$	2			V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 30 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = 520 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA	
	I <sub>DSS</sub>	$V_{DS} = 520 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	20			Α	
Drain-Source On-Resistance <sup>a</sup>	r <sub>DS(on)</sub>	$V_{GS} = 10 \text{ V}, I_{D} = 2 \text{ A}$			650	mΩ	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_{D} = 2 \text{ A}$		15		S	
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_{S} = 7 \text{ A}, V_{GS} = 0 \text{ V}$		0.82		V	
Dynamic <sup>b</sup>							
Total Gate Charge	$Q_g$	$V_{DS} = 100 \text{ V}, V_{GS} = 6 \text{ V},$ $I_{D} = 2 \text{ A}$		29		nC	
Gate-Source Charge	$Q_{gs}$			14			
Gate-Drain Charge	$Q_{gd}$			13			
Turn-On Delay Time	t <sub>d(on)</sub>	$V_{DS}$ = 100 V, $R_{L}$ = 50 Ω, $I_{D}$ = 2 A, $V_{GEN}$ = 10 V, $R_{GEN}$ = 6 Ω		30			
Rise Time	t <sub>r</sub>			13		ns	
Turn-Off Delay Time	t <sub>d(off)</sub>			69			
Fall Time	t <sub>f</sub>			12			
Input Capacitance	C <sub>iss</sub>			3469			
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ Mhz}$		203		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			3			

#### Notes

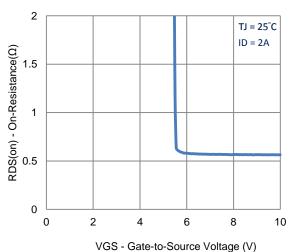
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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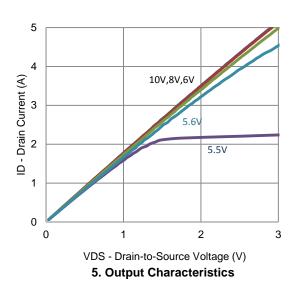
### **Typical Electrical Characteristics**

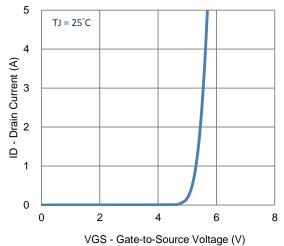


#### 1. On-Resistance vs. Drain Current

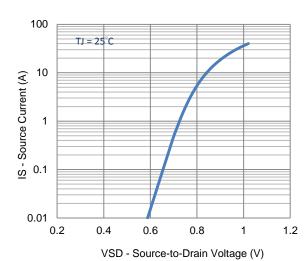


3. On-Resistance vs. Gate-to-Source Voltage

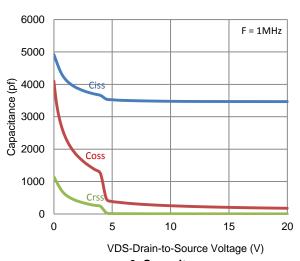




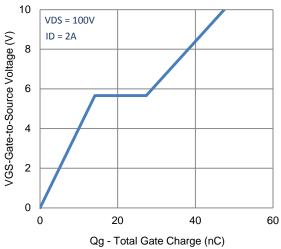
2. Transfer Characteristics

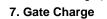


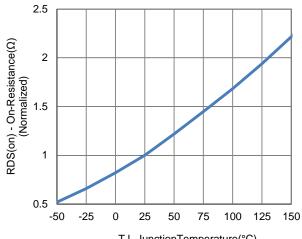
4. Drain-to-Source Forward Voltage



### **Typical Electrical Characteristics**

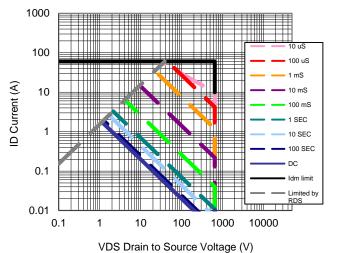




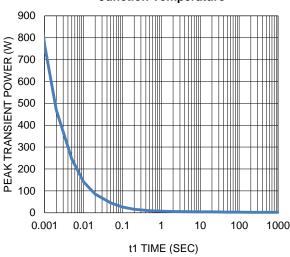


TJ -JunctionTemperature(°C)

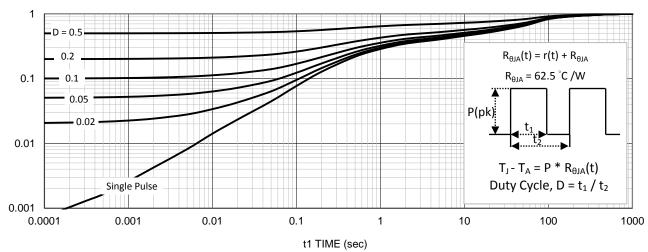




9. Safe Operating Area

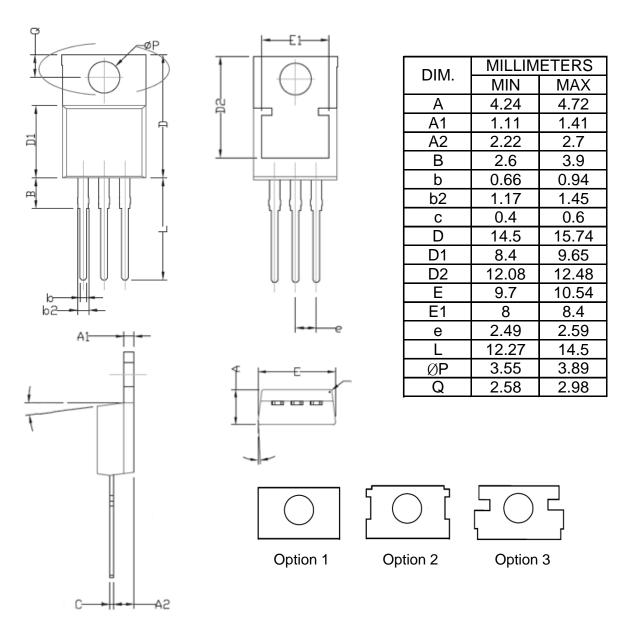


10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

## **Package Information**



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